Intern – RTOS platform

Explore an exciting opportunity to work with a global 3D visionary leader!

Our FARO University Internship Program is designed to equip students with the tools needed to succeed in the 3D world. You will be part of a cohort of students and work on meaningful projects that will add to your professional portfolio and skills. Internships can often lead to professional roles after graduation.

Our interns use their education and newly gained skills on real projects and are embedded in our teams, working side by side with FARO professionals.

As an RTOS platform Intern, you will help us introduce Zephyr as the common RTOS used across all microcontrollers within our FARO Focus product family. You will partake in creating Board Support Packages for both simple STM32-based microcontrollers as well as complex FPGA-SoCs. You will also support us in porting existing code that uses a bare-metal runtime to Zephyr; while doing so, you will contribute in all development phases, starting from design, over implementation, board bring-up and testing up to the deployment of the code in thousands of customer devices. To perform this work, you will become a member of the interdisciplinary Firmware/Embedded Software team, while also closely cooperating with our colleagues from the Electronics team.

In this internship, you will GAIN KNOWLEDGE – Become familiar with core processes and develop working knowledge of industry terminology. You will BUILD RELATIONSHIPS – Experience the business rhythm of the organization through participation in department meetings and observation of team role models. You will PRODUCE RESULTS – complete projects and assignments as given. This is a part time position.

Hiring Preferences:
- Pursuing a Master’s degree in Electrical Engineering, Computer Engineering or related engineering discipline
- Excellent programming skills in C
- Grades well above average
- Good knowledge of RTOS principles and mechanisms
- Familiarity with digital logic design, e.g. ability to understand data sheets, hand-on experience with oscilloscopes or logic analyzers

Desired Skills:
- Practical experience with RTOS- or bare-metal-level programming
- Familiarity with VHDL

Key Behaviors:
- Self-motivated, goal-oriented and eager to learn
- Organized and diligent, even when multi-tasking

Interested? Please contact oswin.horvath@faro.com